

REMARKS

Applicant thanks the Examiner for the very thorough consideration given the present application.

Claims 1-20 are now present in this application. Claims 1, 8, 12, 17, and 20 are independent.

Claims 1, 8, 12, 17, and 20 have been amended. Reconsideration of this application, as amended, is respectfully requested.

Drawings

The Examiner has not indicated whether or not the formal drawings have been approved. Since no objection has been received, Applicant assumes that the drawings are acceptable and that no further action is necessary. Confirmation thereof in the next Office Action is respectfully requested.

Rejection Under 35 U.S.C. § 103

Claims 1, 8, 11, 12, 17, and 20 stand rejected under 35 U.S.C. § 103(a) over U.S. Patent No. 5,784,039 to Yasui and the conventional art, in view of U.S. Patent No. 5,754,155 to Kubota et al., and

claims 2-7, 9-10, 13-16 and 18-19 stand rejected under 35 U.S.C. § 103(a) as being unpatentable over Yasui, the conventional art, and Kubota et al., in view of Lee. These rejections are respectfully traversed.

While not conceding the appropriateness of the Examiner's rejection but merely to advance the prosecution of the present invention,

independent claims 1 and 20 are amended to include, *inter alia*, "simultaneously receiving the gate high voltage and the gate low voltage", and "the switching circuits in the at least one pair are connected by a current control resistor";

independent claim 12 is amended to include, *inter alia*, "a voltage controller for simultaneously receiving the gate high voltage and the gate low voltage", and "first and second switching circuits are connected by a current control resistor"; and

independent claims 8 and 17 are amended to include, *inter alia*, "a switching part for simultaneously receiving the gate high voltage and the gate low voltage", and "the switching part includes at least one pair of switching circuits which are connected by a current control resistor".

Arguments Regarding Deficiencies of Yasui

With regard to Yasui, the Examiner has admitted that Yasui fails to expressly teach "...having the high and low voltages supplied simultaneously". The Examiner relies on the Applicant's conventional art to make up for this deficiency of Yasui.

The Examiner also admits that the combination of Yasui and the Applicant's conventional art does not expressly teach that the voltage controller is disposed between the power supply and the device driving circuit. The Examiner relies on Kubota et al. to make up for the deficiency of the combination of Yasui and the Applicant's conventional art.

Arguments Regarding Deficiencies of Applicant's conventional art

As pointed out by the Applicant's in the Reply of August 20, 2004, the Applicant has made no admission that Fig. 2 of the Applicant's disclosure qualifies as statutory prior art.

As pointed out by the Applicant's in the Reply of August 20, 2004, the Applicant's disclosure provides that "when a main power VDD is supplied to the power block 10, the gate low voltage VGL and the gate high voltage VGH are simultaneously output from the power block 10 as shown in Fig. 2." It indeed appears that said voltages are output from the power block 10 (see Applicant's Fig. 1). However, these voltages are not both received by a voltage controller,

disposed between the power supply and the device driving circuit. As shown in the Applicant's Fig. 1, VGL is not supplied to a voltage controller, but rather, VGL is supplied to gate driver 6 (directly) from power block 10. In this instance, simultaneously output does not translate into simultaneously received.

Therefore, neither Yasui, nor the Applicant's conventional art teaches or suggests the combinations recited in independent claims 1, 8, 12, 17, and 20, including simultaneously receiving the gate high voltage and the gate low voltage.

Arguments Regarding Deficiencies of Kubota et al.

The Examiner asserts that Kubota et al. (FIGS. 1 and 2) disclose power supply circuit 11a and a voltage control circuit (the voltage generating circuit 12a and the current supply circuit 13) disposed between the power supply and the driving circuit 3 for supplying the VGH and VGL to the driving circuit 3.

A careful review of Kubota et al. FIG. 2 merely discloses a power supply circuit having a reference voltage generating circuit 12a including two circuit TR_(pix), each TR_(pix) is directly connected to a buffer amplifier 14 of the current supplying circuit 31a.

Nowhere in the Kubota et al. document is there any hint of two switching circuits connected by a current control resistor (as set forth in each of independent claims 1, 8, 12, 17, and 20 of the present invention).

Thus, neither the combination of Yasui and Applicant's conventional art, nor the combination of Yasui, Applicant's conventional art, and Kubota et al., can suggest the present invention as set forth in independent claims 1, 8, 12, 17, and 20. Reconsideration and withdrawal of these art grounds of rejection are respectfully requested.

With regard to dependent claims 2-7, 9-11, 13-16 and 18-19, Applicant submits that claims 2-7, 9-11, 13-16 and 18-19 depend, either directly or indirectly, from independent claims 1, 8, 12 and 17, which are allowable for the reasons set forth above, and therefore, claims 2-7, 9-11, 13-16 and 18-19 are allowable based on their dependence from claims 1, 8, 12 and 17, or due to the novel features set forth therein. Reconsideration and allowance thereof are respectfully requested.

Conclusion

All of the stated grounds of rejection have been properly traversed, accommodated, or rendered moot. Applicant therefore respectfully requests that the Examiner reconsider all presently outstanding rejections and that they be withdrawn. It is believed that a full and complete response has been made to the outstanding Office Action, and as such, the present application is in condition for allowance.

If the Examiner believes, for any reason, that personal communication will expedite prosecution of this application, the Examiner is invited to telephone Carl T. Thomsen, Registration No. 50,786, at (703) 205-8034, in the Washington, D.C. area.

Prompt and favorable consideration of this Amendment is respectfully requested.

Pursuant to 37 C.F.R. §§ 1.17 and 1.136(a), the Applicants respectfully petition for a three (3) month extension of time for filing a response in connection with the present application and the required fee of \$1,020.00 is attached herewith.

Application No. 10/025,477
Response to Office Action of February 22, 2005

Attorney Docket No. 2658-0279P
Art Unit: 2675
Page 18 of 18

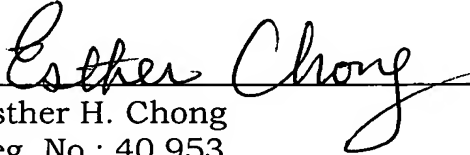
If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. §§ 1.16 or 1.17; particularly, extension of time fees.

August 15, 2005

Respectfully submitted,

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